FACT SHEET

Callisto Single Board Computer

KEY FEATURES

General

- Artix-7 FPGA SoM Mezzanine Card:
  - 1 GB DDR SDRAM
  - 32 MB QSPI Flash Memory
  - 10/100 Mbs Ethernet PHY

Ecosystems

- Configurable with Zynq® Ultrascale+ SoM Mezzanine Card
- Up to 4 PCIe or similar lanes on 8 SERDES differential pairs
- Additional I/O with single-ended and differential FPGA GPIO pins

Radiation Tolerance

- 100 KRad TID built-in latchup protection

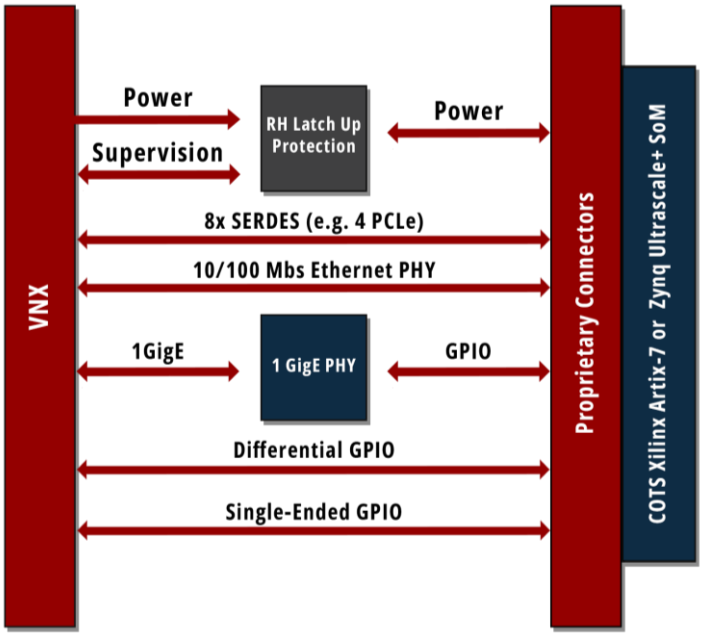
OVERVIEW

The IDEAS-TEK designed the Galileo Callisto™ FPGA-Based Single Board Computer to provide high-performance COTS computing power with built-in latchup protection for Galileo hardware systems. Callisto is housed within a conduction-cooled VNX 19mm module and communicates via the Galileo high-speed backplane.

Callisto supports a Trenz Electronics Artix®-7 FPGA SoM or a Zynq® Ultrascale+ FPGA SoM as a mezzanine card.

COMPLIANCE

The RTVS is backplane-based modular system aligned with VNX+ (VITA 74) to facilitate re-use and interoperability during the implementation of fault-tolerant, high-performance systems for spacecraft and other high-availability applications.



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